**Consider the following Memory Area and give the answers of the following questions:-**

**0C21:01A0** 13 96 D0 E0 D0 E0 A2 1E-99 80 3E 20 99 00 75 24 ..........> ..u$

**0C21:01B0** A2 24 99 0A C9 75 1D 0A-C0 74 19 8B 0E 21 96 E3 .$...u...t...!..

**0C21:01C0** 13 B0 1A 06 33 FF 8E 06-00 96 F2 AE 07 75 05 4F ....3........u.O

**0C21:01D0** 89 3E 21 96 BB 06 97 80-3E 13 96 00 74 03 BB 4C .>!.....>...t..L

**0C21:01E0** 97 BE C3 96 8B 3E 05 98-B9 08 00 AC 3C 3F 75 02 .....>......<?u.

**0C21:01F0** 8A 07 3C 20 74 01 AA 43-E2 F1 B1 03 B0 20 38 04 ..< t..C..... 8.

**0C21:0200** 74 12 B0 2E AA AC 3C 3F-75 02 8A 07 3C 20 74 01 t.....<?u...< t.

**0C21:0210** AA 43 E2 F1 32 C0 AA C3-F6 46 04 02 75 43 8B A3 .C..2....F..uC..

**i)** Which Logical address (i.e segment and offset address) in the above Memory area is equal to Physical address 0C3EA?

**ii)** what is the new value of register IP, after executing a 4 Byte size instruction residing at offset address 01E0?